

**Inventor Information**

Inventor One Given Name:: Terunao  
Family Name:: HANAOKA  
Name Suffix::  
City of Residence:: Suwa-shi  
State or Prov. of Residence:: Nagano-ken  
Country of Residence:: Japan  
Inventor Two Given Name:: Haruki  
Family Name:: ITO  
Name Suffix::  
City of Residence:: Chino-shi  
State or Prov. of Residence:: Nagano-ken  
Country of Residence:: Japan  
Inventor Three Given Name:: Kazuhiko  
Family Name:: NOZAWA  
Name Suffix::  
City of Residence:: Nakata-shi  
State or Prov. of Residence:: Yamagata-ken  
Country of Residence:: Japan  
Inventor Four Given Name::  
Family Name::  
Name Suffix::  
City of Residence::  
State or Prov. of Residence::  
Country of Residence::  
Inventor Five Given Name ::  
Family Name::  
Name Suffix::  
City of Residence::  
State or Prov. of Residence::  
Country of Residence::

**Correspondence Information**

Name Line One:: Oliff & Berridge PLC  
Address Line One:: P.O. Box 19928  
City:: Alexandria  
State or Province:: VA  
Postal or Zip Code:: 22320  
Telephone:: (703) 836-6400  
Fax:: (703) 836-2787  
Electronic Mail:: commcenter@oliff.com

**Application Information**

Title Line One:: SEMICONDUCTOR DEVICE AND METHOD OF  
Title Line Two:: MANUFACTURE THEREOF, CIRCUIT  
Title Line Three:: BOARD, AND ELECTRONIC INSTRUMENT  
Title Line Four::

Total Drawing Sheets:: 13  
Docket Number:: 107284

**Continuity Information**

>This application is a::  
Application One::  
Filing Date::  
Patent Number::  
which is a::  
>>Application Two::  
Filing Date::  
Patent Number::

**Prior Foreign Applications**

Foreign Application One:: JP 11-069420(P)  
Filing Date:: March 16, 1999  
Country:: Japan  
Priority Claimed:: yes  
Foreign Application Two:: JP 11-069421(P)  
Filing Date:: March 16, 1999  
Country:: Japan  
Priority Claimed:: yes  
Foreign Application Three::  
Filing Date::  
Country::  
Priority Claimed::